

iSTART

EZ-NBIST for eFlash, ReRAM and MRAM BIST/BISR



INNOVATION
EFFICIENCY
SERVICE



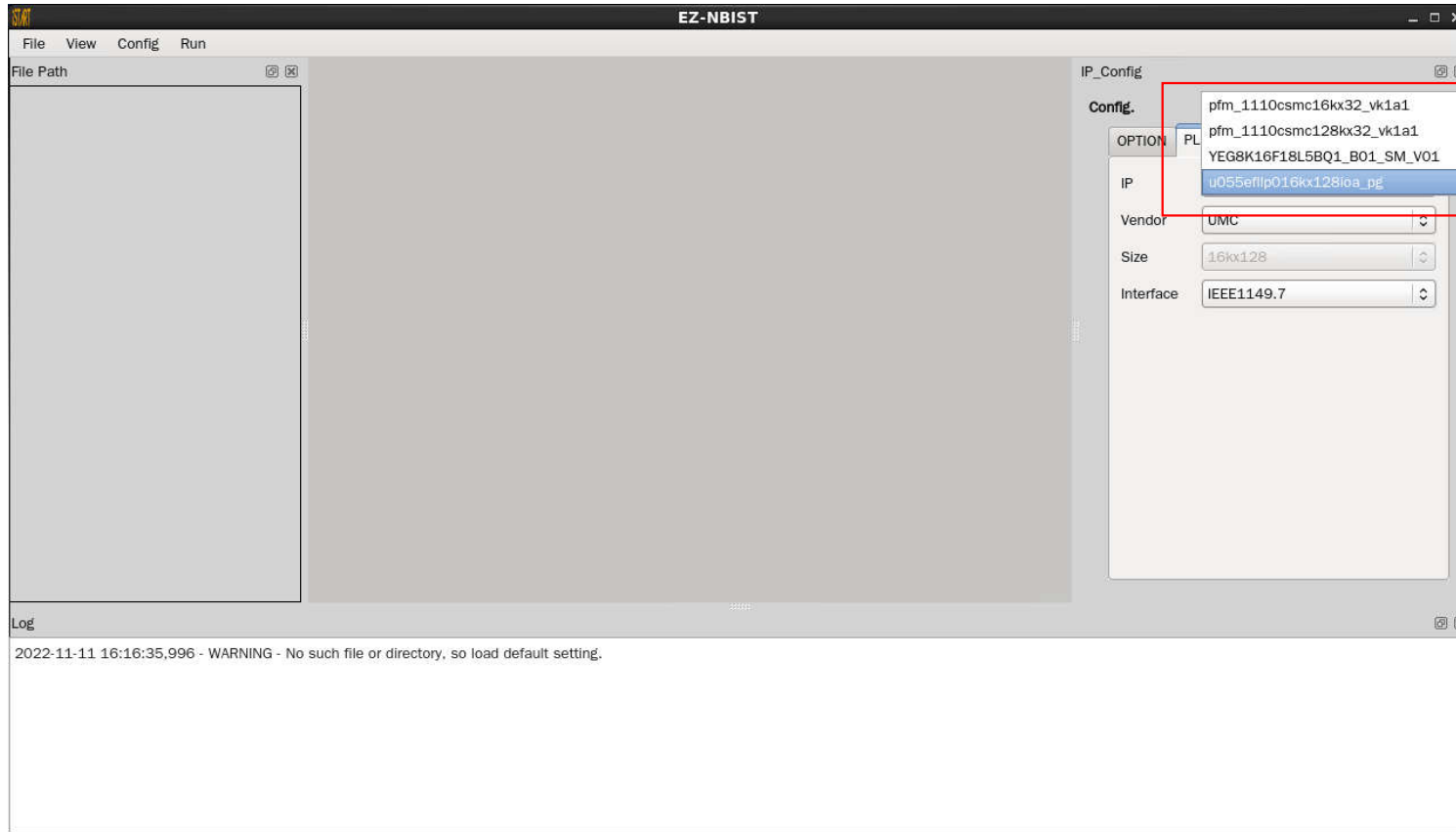
Customized EDA tool : EZ-NBIST

Functions	EZ-NBIST customized EDA tool
Operating method	GUI mode
NVM IP Vendor	UMC (eFlash)
NVM IP Size	16Kx128 (UMC eFlash) 16Kx32 (UMC eFlash) 64Kx144 (UMC eFlash)
Interface	JTAG IEEE1149.7 SPI
NVM IP Testing Items	All
Security Protection	Customized

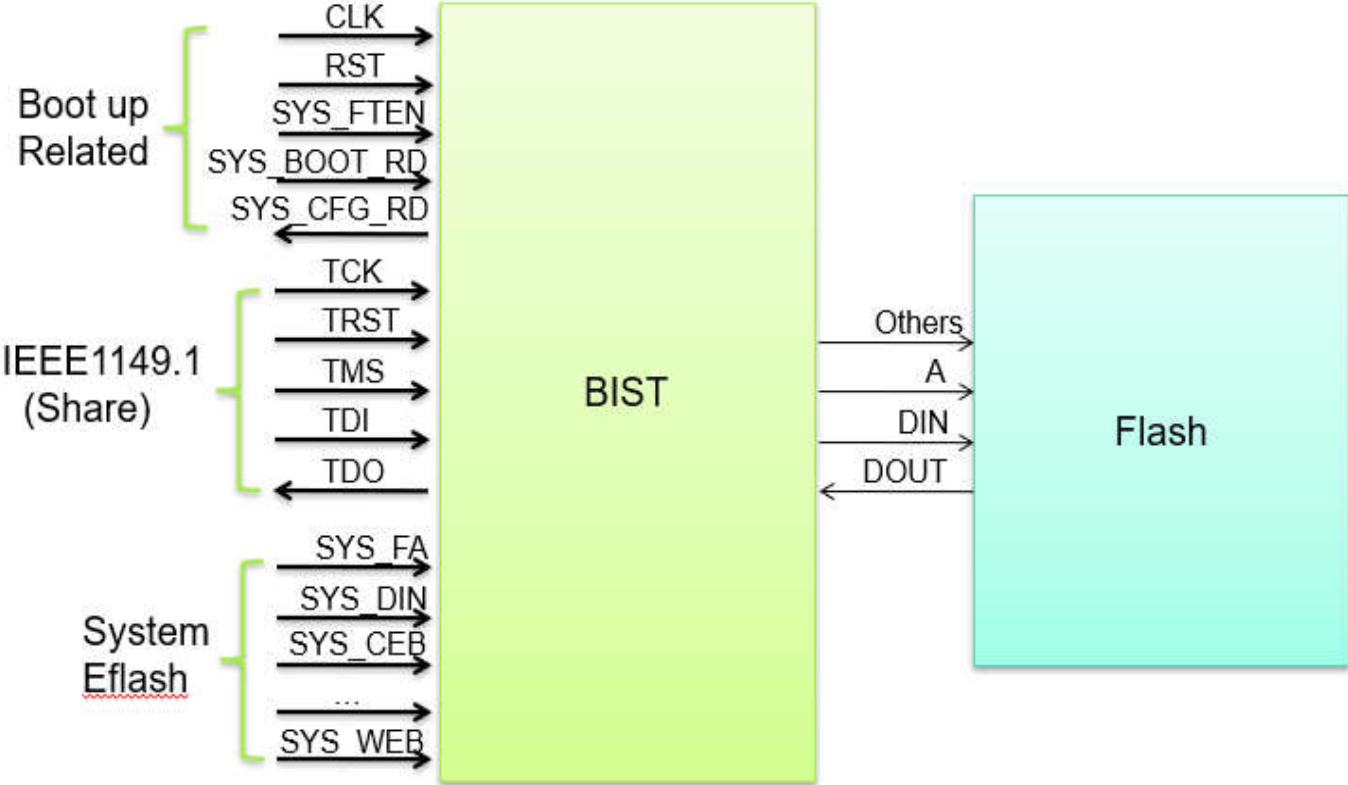
Customized EDA tool : EZ-NBIST

Functions	EZ-NBIST customized EDA tool
Operating method	GUI mode
NVM IP Vendor	SST (eFlash) Vendor (ReRAM)
NVM IP Size	128Kx32 (SST eFlash) 16Kx32 (SST eFlash) 512Kx32 (ReRAM)
Interface	IEEE1149.7 JTAG SPI
NVM IP Testing Items	All
Security Protection	Customized

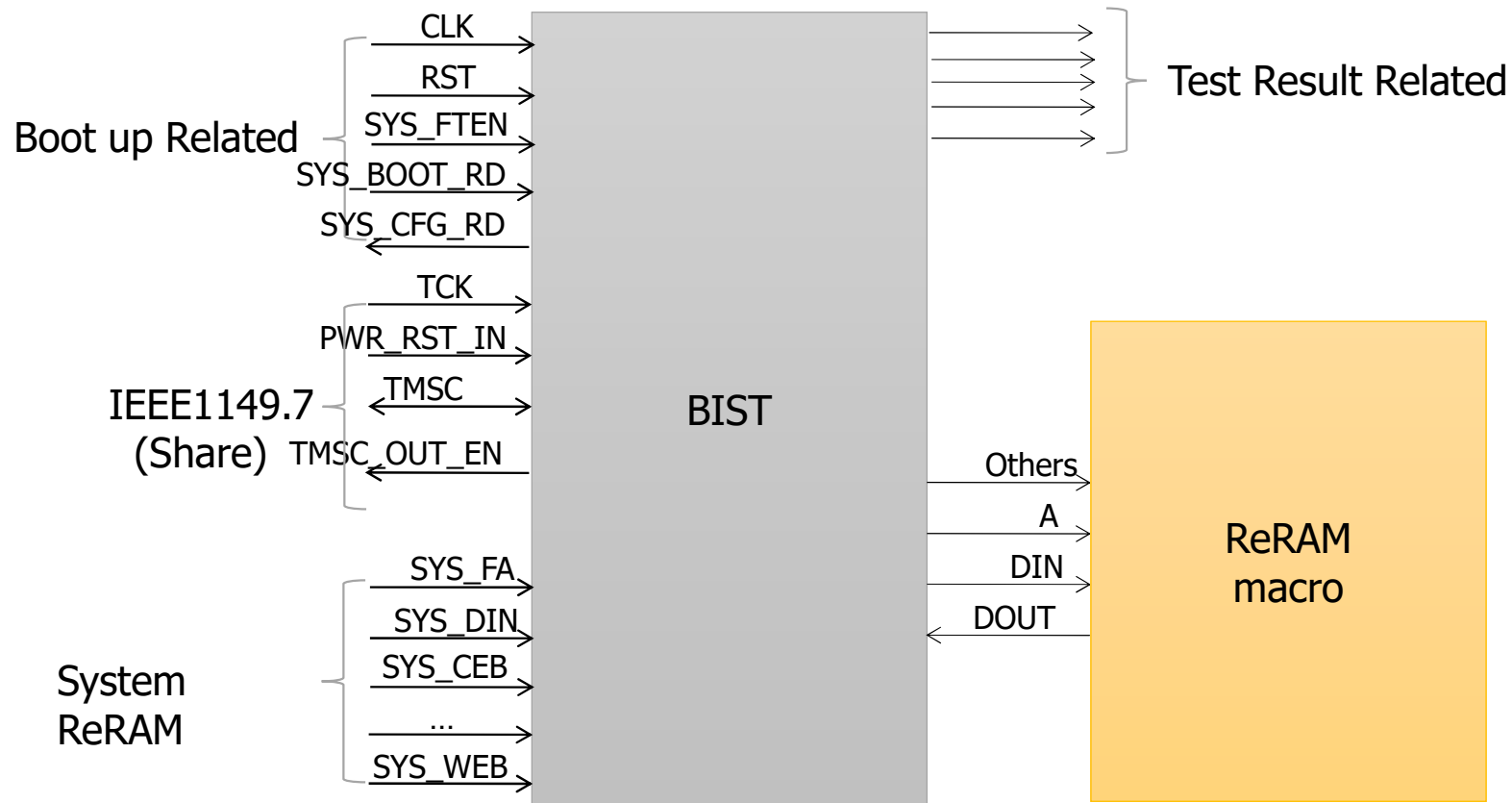
EZ-NBIST : PLATFORM -> IP



eFlash Test and Repair Block with JTAG



ReRAM Test and Repair Block with IEEE1149.7



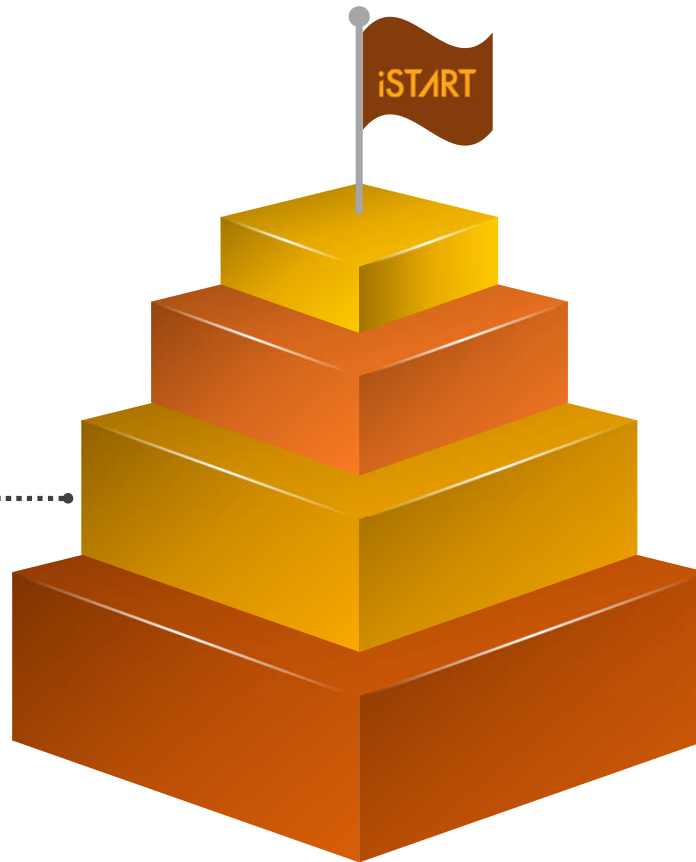
MRAM Testing Algorithm

◆ **START**

- March RP
- March 17N
- UDA : User Define Testing Algorithm

Conclusion

- ◆ EZ-NBIST provides eFlash BIST/BISR with professional testing items
- ◆ EZ-NBIST saves eFlash tuning timing testing time in ATE



- ◆ EZ-NBIST is easy to set for accomplishing eFlash IP's testing circuit
- ◆ eFlash BIST/BISR area overhead is acceptable



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Thank You

START Boosting Competitiveness of Your Products